#### Universal Turing Machines

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- Can one Turing machine simulate every TM?

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#### Universal TM

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• A UTM can be designed to simulate the computations of an arbitrary TM M. To do so, input to UTM must contain representation of both - machine *M* and string *w* processed by *M*.



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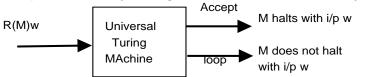
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- Output1: Accept (indicates that *M* halts with input *w*), output2: loops, i.e., *M* does not halt with input *w*, i.e. computation of does not terminate
- The machine *U* is called universal TM, as computation of any Turing machine can be simulated by *U*.



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Symbol	Encoding
0	1
1	11
#	111
$q_0$	1
$q_1$	11
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• let en(z) denote the encoding of z. Hence, Transition  $\delta(q_i, a) = (q_j, b, d)$  is encoded by string  $en(q_i)0en(a)0en(q_j)0en(b)0en(d)$ . 0 separates the components of  $\delta$ .

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#### **3-tape Deterministic TM U:**

 Tape-1 holds R(M)w. Tape-3 simulates computations of of M for input w. Tape-2 as working tape.

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